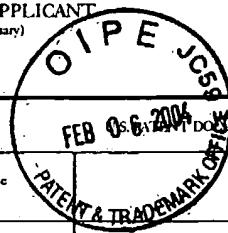
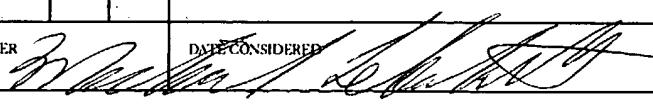
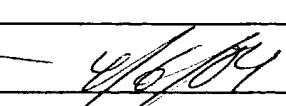


Form PTO-1449				U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1599		SERIAL NO. 09/820,468	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)						APPLICANT Werner Juengling et al.			
						FILING DATE March 28, 2001		GROUP 2824	
FEB 05 2001 U.S. PATENT DOCUMENTS									
*Examiner Initial		Document Number	Date	Name		Class	Subclass	Filing Date If Appropriate	
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	AM							Yes	No
	AN								
	AO								
	AP								
	AQ								
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)									
	AR			Wolf et al., "Silicon Processing for the VLSI Era, Vol. 1-Process Technology, Lattice Press: Sunset Beach CA, 1986, pp. 428-429.					
	AS								
	AT								
EXAMINER 	DATE CONSIDERED 								
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